



Home-Monitoring LiDAR Sensors Using On-Chip Avalanche Photodiode

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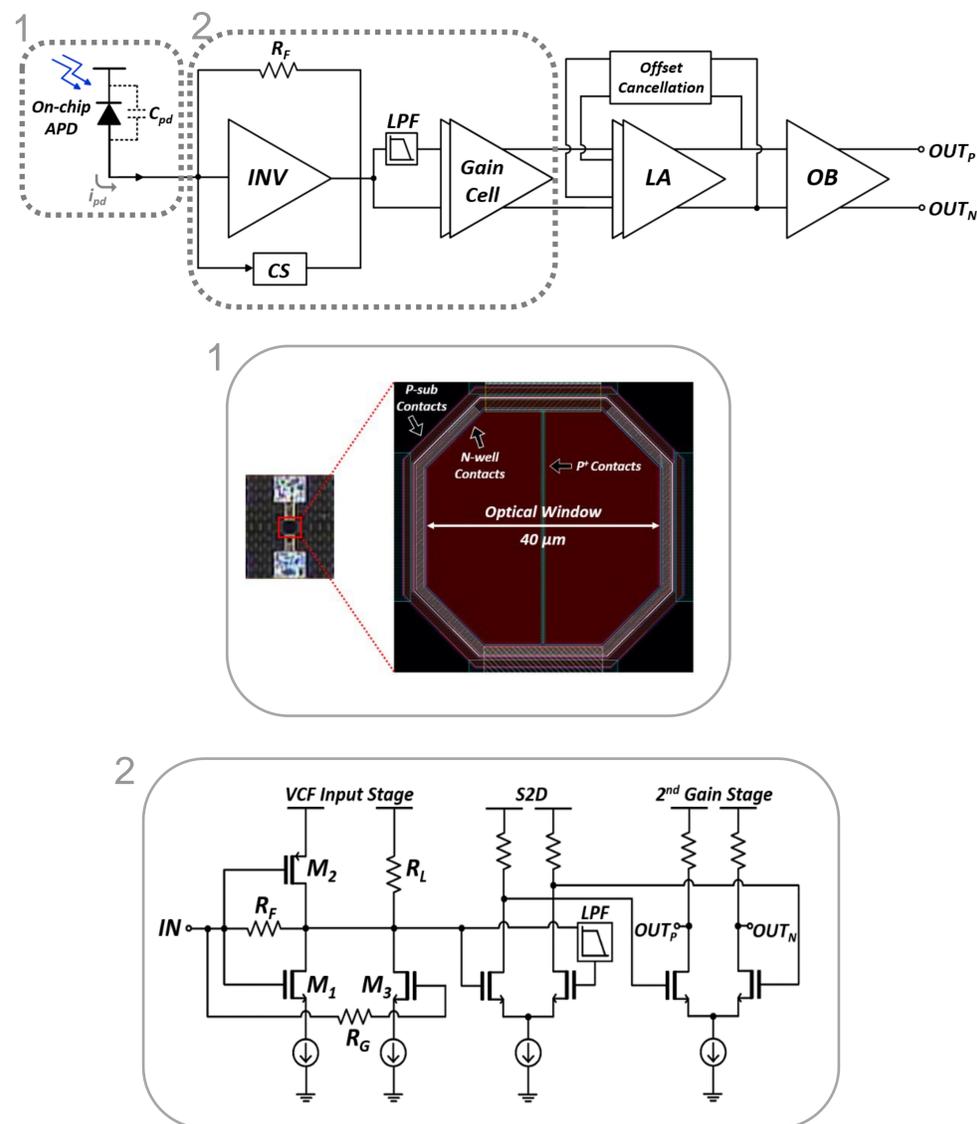
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Introduction

- Multi-channel optoelectronic IC targeting for LiDAR elder-care systems
- Fabricated in a standard 180-nm CMOS technology
- On-chip APD arrays implemented to investigate responsivity and breakdown voltage

OEIC Description



- On-chip APD monolithically integrated on Rx IC
- VCF-TIA with twice higher transimpedance gain than INV-TIA for a similar bandwidth

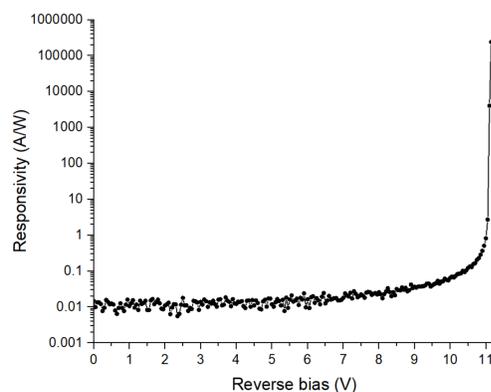
Conclusion

- Proposed OEIC demonstrates wide and clean eye diagrams up to 800 Mb/s.
- On-chip APD functions successfully, demonstrating 2.72-A/W responsivity at 11.05-V bias voltage.

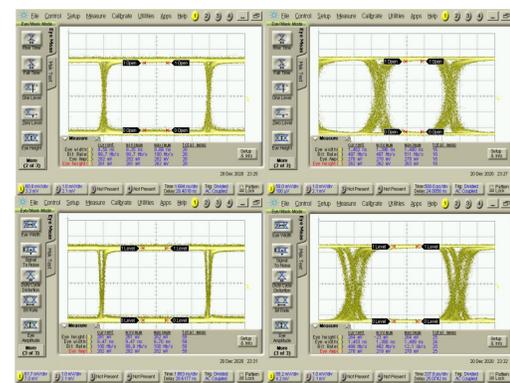
Acknowledgement

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Measured Results

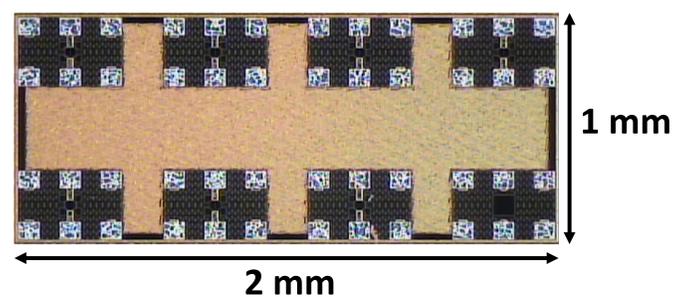


Measured responsivity of the P+/N-well APD



Measured eye-diagrams at 100 Mb/s & 500 Mb/s for $50\ \mu\text{A}_{pp}$ and $500\ \mu\text{A}_{pp}$ inputs

APD Array Chip Photograph



OEIC Array Chip Photograph

